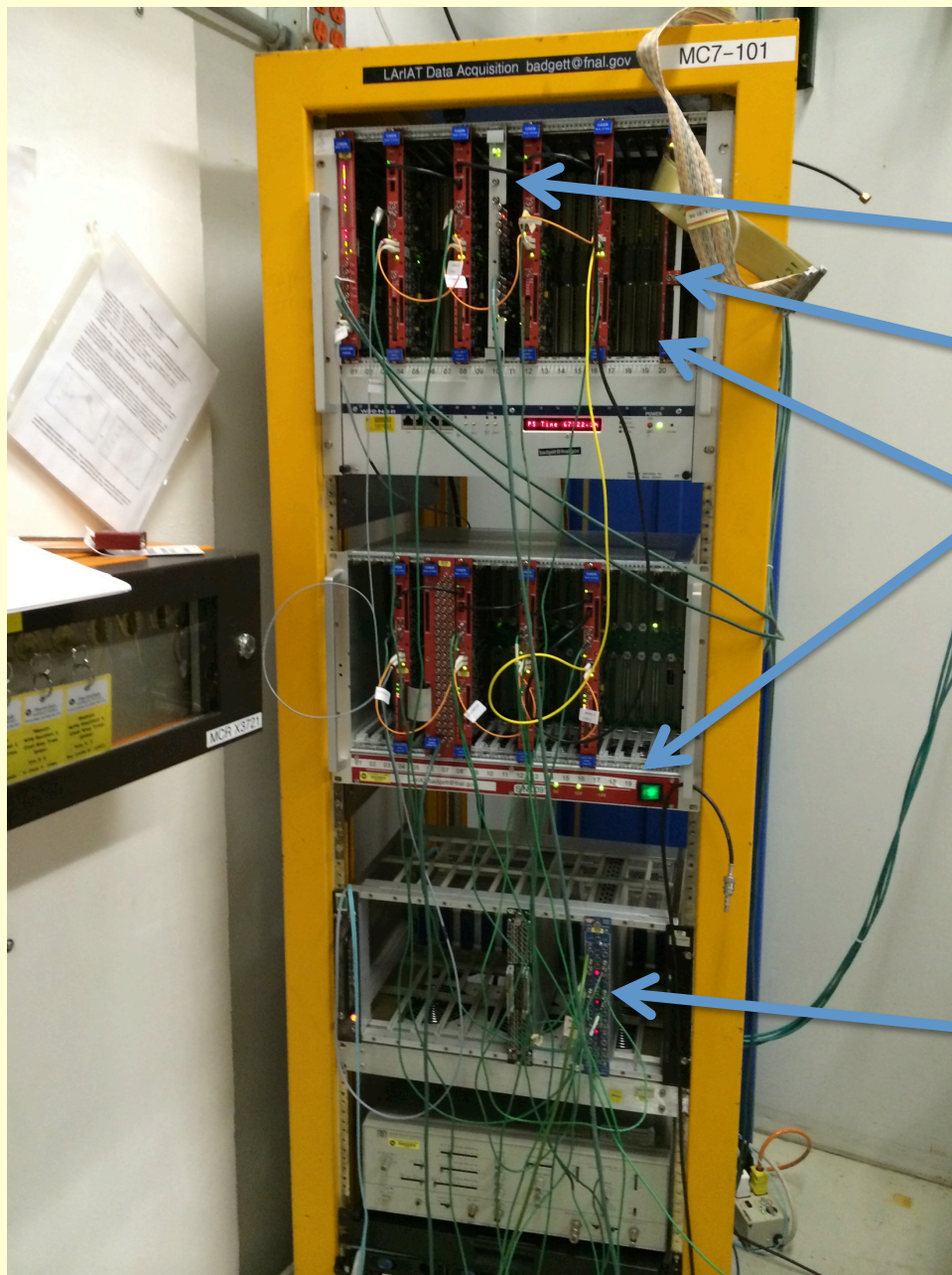


LArIAT TPC Readout



Jinyuan TDC

Mike V1495

Full complement of 8
V1740, chained clock
and optical control

Trigger NIM Fanout

LArIAT V1740s



Ran full eight board readout

- Hours without errors
- Common Trigger with NIM fanout
- Single master (0) for clock chain and IRQ production
- All up-to-date firmware

Need to:

- Synchronize clock chain
 - Requires careful board by board offset measurement and setting with CAENUpgradeGUI
- Test inter-board synch
- Use more realistic, wider pulse from generator
- Implement common start
- Integrate with V1495 trigger card

Jinyuan WU TDC



Have new firmware version

- New channel disable mask
- Implement VME block transfer fix
 - Odd behavior during test – hangs up – needs more info
- Introduced word count bug (to be fixed)
- Noisy channel disappeared (!!)
- Have two fully functional cards now
- Buffer page swap now fully deadtimeless